REMARKS/ARGUMENTS

In paragraph 2, on page 2 of the Office Action dated April 10, 2003 in the current application, the Examiner requested submission of an Information Disclosure Statement (IDS) citing all of the references that were cited in the parent of the current application, namely U.S. Patent 6,292,765. Accordingly, attached hereto is an IDS statement (2 pages) and three sheets of PTO form 1449 (labeled as part A) citing a total of 33 references. Copies of most of these references are attached to the IDS, except for three references identified as nos. 9, 10 and 11 at the bottom of part A sheet 1 of the attached form 1449. These three references are not currently available to the undersigned attorney. However, since these three references were cited by the US Patent and Trademark Office (USPTO) during prosecution of U.S. Patent 6,292,765, they should be readily available to the current Examiner, at least in the USPTO files of the parent case, namely U.S. Patent 6,292,765.

In addition, the current Examiner is respectfully requested to carefully review the file history of U.S. Patent 6,292,765 to take into account all the arguments being made for and against patentability in the various office actions and amendments therein.

Applicants also request the Examiner to review the references cited in file histories of several related patents and patent application: US Patent 6,175,946, US Patent 6,609,229 and the currently-pending US Patent Application 10/348,116 (these three cases are also called herein as the "checkers" family). A copy of PTO forms 1449_listing of references cited in the "checkers" family is attached hereto, and has been labeled as part B sheets 1-24. These references are believed to be relevant to a number of claims in the current application, e.g. see Claims 15 and 78 (this is an exemplary list and not exhaustive, and the Examiner is requested to carefully review all of the above claims). Copies of the references from the "checkers" family are not attached hereto, because they are readily available to the current Examiner in the USPTO files for the "checkers" family.

The current Examiner is respectfully requested to carefully review the file history of the "checkers" family to take into account all the arguments being made for and against patentability in the various office actions and amendments therein.

In the above-identified Office Action, Claims 11-13 were indicated as being rejected for double patenting over the parent of the current application, namely U.S. Patent

SILICON VALLEY PATENT GROUP LLP

2350 Mission College Blvd Suite 360 Santa Clara, CA 95054 (408) 982-8200 FAX (408) 982-8210 6,292,765. See paragraph 3 on page 2 of the Office Action. Accordingly, Applicants submit herewith a <u>Terminal Disclaimer</u> to overcome the double patenting rejection. Accordingly previously-presented Claims 11-13 are believed to be in form for allowance. New Claims 14-15 depend from Claim 11 and are therefore also believed to be allowable.

New Claims 16-52 are similar (but not identical) to Claims 1-37 that were granted in the parent of the current application, namely U.S. Patent 6,292,765. Accordingly, as noted above, the Examiner is respectfully requested to review the file history of U.S. Patent 6,292,765. Thereafter, the Examiner should independently examine the patentability of each claim being presented in the current amendment in view of all of the references of record at the time of examination of the claims (including but not limited to references cited in the above-mentioned IDS and any future IDS that may be filed prior to the next Office Action).

New Claims 53-79 are believed to be allowable for reasons similar to the reasons for the allowability of Claim 11.

For the above reasons, Applicants respectfully request allowance of all pending claims 11-79. Should the Examiner have any questions concerning this response, the Examiner is invited to call the undersigned at (408) 982-8200, ext. 3.

Via Express Mail Label No. ER 205 704 792 US

Respectfully submitted,

Omkar K. Suryadevara Attorney for Applicants Reg. No. 36,320

SILICON VALLEY
PATENT GROUP LLP

2350 Mission College Blvd Suitz 360 Santa Ctara, CA 95054 (408) 982-8200 FAX (408) 982-8210